

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination HALCOMB ET AL.	
		Examiner Russell L. Guill	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Brynjolfson, Ian; Zilic, Zeljiko; "Dynamic Clock Management for Low Power Applications in FPGAs", 2000, Proceedings of the IEEE 2000 Custom Integrated Circuits Conference, 21-24 May 2000 Page(s):139 – 142
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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